

**This homework is due on May 14, 2019 at 6pm in class.**

Page size = frame size

Logical address space (size) =  $2^m$

Physical address space (size) =  $2^x$  (where x is the number of bits in physical address)

Logical address space (size) = # of pages  $\times$  page size

Physical address space (size) = # of frames  $\times$  frame size

Page size = frame size =  $2^n$

# Of pages =  $2^{m-n}$

# Of entries (records) in page table = # of pages

### Question 1

Consider a computer system with a 32-bit logical address and 4-KB page size. The system supports up to 512 MB of physical memory. How many entries are there in each of the following?

- A conventional single-level page table.

# of pages = # of entries = ?

Size of logical address space =  $2^m$  = # of pages  $\times$  page size

$2^{32}$  = # of pages  $\times 2^{12}$

# Of pages =  $2^{32} / 2^{12} = 2^{20}$  pages

### Question 2

Consider a logical address space of 256 pages with a 4-KB page size, mapped onto a physical memory of 64 frames.

a. How many bits are required in the logical address?

Method 1

Logical address space (/size) =  $2^m$

Logical address space (/size) = # of pages  $\times$  page size

Logical address space (/size) =  $256 \times 4$  KB

Logical address space (/size) =  $256 \times 4096$

Logical address space (/size) = 1048576

Logical address space (/size) =  $2^{20}$

$m = 20$  (Answer)

Method 2

Page size =  $2^n$

$4096 = 2^{12}$

$n = 12$

# of pages =  $2^{m-n}$

$256 = 2^{m-12}$

$2^8 = 2^{m-12}$

$2^{8+12} = 2^m$

$$m = 20$$

b. How many bits are required in the physical address?

Let  $x$  be the number of physical addresses

$$\text{Physical address space (size)} = 2^x$$

$$\text{Physical address space (size)} = \# \text{ of frames} \times \text{frame size}$$

$$\text{Physical address space (size)} = 64 \times 4 \text{ KB}$$

$$\text{Physical address space (size)} = 64 \times 4096$$

$$\text{Physical address space (size)} = 2^6 \times 2^{12} = 2^{18}$$

$$\text{Number of required bits in the physical address} = x = 18 \text{ bit}$$

### Question 3

Assuming a 1-KB page size, what are the page numbers and offsets for the following address references (provided as decimal numbers):

- 3085
- 42095
- 215201
- 650000
- 2000001

$$\text{Answer size} = 2^n = 1\text{KB} = 1024 = 2^{10}$$

$$\# \text{ of bits in offset part (n)} = 10$$

Solution steps:

- Convert logical address: Decimal to Binary
- Split binary address to 2 parts (page #, Offset), offset: n digits
- Convert offset & page #: Binary to Decimal

a. 3085

$$\text{Decimal} = 3085$$

$$\text{Binary} = 000000110000001101$$

$$\text{Page \#} = 011 = 3$$

$$\text{Page offset} = 0000001101 = 13$$

b. 42095

$$\text{Decimal} = 42095$$

$$\text{Binary} = 1010010001101111$$

$$\text{Page \#} = 101001 = 41$$

$$\text{Page offset} = 0001101111 = 111$$

c. 215201

$$\text{Decimal} = 215201$$

$$\text{Binary} = 110100100010100001$$

$$\text{Page \#} = 11010010 = 210$$

$$\text{Page Offset } 0010100001 = 161$$

d. 650000

Decimal: 650000

Binary: 10011110101100010000

Page #: 1001111010 = 634

Page Offset: 1100010000 = 784

e. 2000001

Decimal

Binary: 111101000010010000001

Page # = 11110100001 = 1953

Page Offset: 0010000001 = 129

#### Question 4

What is the purpose of paging the page tables?

#### Question 5

Suppose that a disk drive has 5,000 cylinders, numbered 0 to 4,999. The drive is currently serving a request at cylinder 2,150, and the previous request was at cylinder 1,805. The queue of pending requests, in FIFO order, is:

2,069, 1,212, 2,296, 2,800, 544, 1,618, 356, 1,523, 4,965, 3681

Starting from the current head position, what is the total distance (in cylinders) that the disk arm moves to satisfy all the pending requests for each of the following disk-scheduling algorithms?

- a. FCFS
- b. SSTF
- c. SCAN
- e. C-SCAN